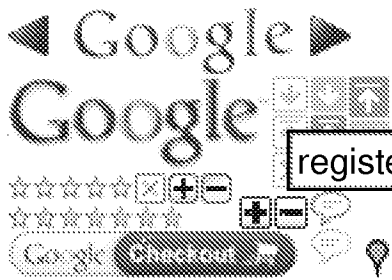


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Dead code elimination. X. X. Value numbering. X. X. Local constant propagation. X. X. Global commoning, **code** motion. X. X. **Register allocation** (k = r-4) ...

www.cs.rice.edu/~keith/512/Lectures/21PL8.pdf - [Similar](#)

2. [Generating efficient **code** for a computer with dissimilar **register** ...](#)

Lastly, the standard optimization of "**dead code** elimination" is done as shown in block 6 of Fig. 1, and then **register allocation**, as shown in block 7 in Fig ...

www.freepatentsonline.com/EP0373361.html - [Similar](#)

by ME Hopkins - 1996 - [Cited by 17](#) - [Related articles](#) - [All 4 versions](#)

3. [Generating efficient **code** for a computer with dissimilar **register** ...](#)

2 is completed, **dead code** in the program is removed in block 6. This is desirable because the steps ... 2 is made over the program in which "**fix up**" **code** is inserted. ... 1, and then **register allocation**, as shown in block 7 in FIG. 1. ...

<https://www.patentstorm.us/patents/4961141/description.html> - [Similar](#)

4. [w9.ppt](#)

Code scheduling and **register allocation** in basic blocks were already two optimization ... applying **dead code** elimination before constant propagation, ...

cobweb.ecn.purdue.edu/~smidkiff/ece495S/files/.../w9Color.pdf - [Similar](#)

5. [A spill **code** minimization technique--application in the Metrowerks ...](#)

INTRODUCTION Global **register allocation** and spilling using ... In order to minimize the amount of **fix-up code** between tiles, we propagate individual ... to remove store instructions that are useless (they actually become **dead code**). ...

[goliath.ecnext.com/.../A-spill-code-minimization-technique.html](#) - [Cached](#) - [Similar](#)

6. [archive.netbsd.se](#)

The **register allocation** algorithm example.c uses is extremely noddy but seems ...
Expand stage is mostly for some **fix up**. E.g. remove obvious **dead code** like ...

[archive.netbsd.se/?ml=linux-sparse&a=2008-06&t...](#) - [Cached](#) - [Similar](#)

7. [OPTIONS -fno-warn-missing-signatures ...](#)

regm ([instr] -- instructions after **register allocation** , [natbasicblock instr]) -- fresh
blocks of **fixup code**. linearra _ accinstr accfixup _ [] = return ...

[darcs.brianweb.net/d.h.o/ghc/compiler/nativeGen/.../Main.hs](#) - [Cached](#) - [Similar](#)

8. [PS] [Local Register Allocation](#) Spanning Basic Blocks

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variable live ranges and deals with **dead code** elimination. The **register allocation**
step. assigns registers for local uses and defs of instructions. ...

[historical.ncstrl.org/litesite-data/cgi_cse/96-TH-002.ps.gz](#) - [Similar](#)

by L. Thompson - 1996 - [Related articles](#)

9. [PDF] [PowerPoint Presentation](#)

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Weird interactions with **register allocation** ... could ... (AKA Dynamic **Dead Code**)

But, **fix-up code** may cause the remaining 20% of the time to be ...

[www.cc.gatech.edu/~milos/CS6290F07/8_StaticScheduling.pdf](#) - [Similar](#)

10. [PPT] [Parametric Shape Analysis via 3-Valued Logic](#)

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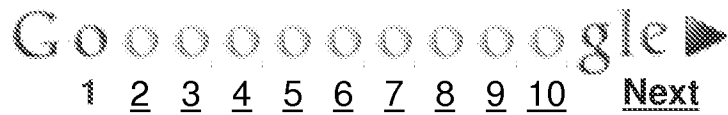
Dead code elimination. Software pipelining, ... Instruction Scheduling 1. **Register allocation**. Instruction Scheduling 2. Intraprocedural l-cache optimizations ...

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